EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	143781	hierarch$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L2	99256	simulat$4.ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L3	6039661	join$4 combine$4 merg$4 blend$4 meld$4 fus$4 aggreg$5 match$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L4	1198572	leaf node	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L5	34702	L3 near3 L4	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L6	111383	"716" 703/13 703/14	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L7	9515	L6 and L1 and L3	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L8	724	L5 and L7	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45

EAST Search History

L9	39	L8 and L2	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L10	367	leaf adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L11	21	L10 and L6	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L12	26	pair and L9	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L13	12	(identical near3 L4) and L6 and L2	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L14	11	L12 and split	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45
L15	3	port adj connectivity adj interface	US-PGPUB; USPAT; USOCR; FPRS; EPO; DERWENT; IBM_TDB	OR	ON	2008/01/28 18:45

Google Search Notes

Jan 28, 2008
Searched for simulation OR emulation circuit functional OR behavior grouping OR merging

Searched for simulation OR emulation merge circuit identical OR same OR isomorphic OR close behavior OR function OR results - Viewed 3 resultsViewed 3 results

Not starred Automated load determination for partitioned simulation - US ... -
patentstorm.us - See 1 more pageSee 1 more page - Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/-patents/6553543-claimm.html

Not starred http://www.patentstorm.us/patents/6658636-description.html -
Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/patents/-6658636-description.html

Not starred http://www.patentstorm.us/patents/6658636-description.html -
Bookmark: edit - remove

Labels:
Notes:
www.patentstorm.us/patents/-6658636-description.html

Not starred System and method for adaptive partitioning of circuit... -
freepatentsonline.com - Bookmark: edit - remove

Labels:
Notes:
www.freepatentsonline.-com/7024652.html

Searched for merging circuits same OR identical behavior simulation OR emulation

m Searched for merging circuits same behavior simulation OR emulation

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Searched for merging circuits same bavhior simulation OR emulation

Searched for connection merging circuits pin simulation OR emulation

Searched for port connection merging circuits pin simulation OR emulation

Searched for port connection merging circuits pin

Searched for port connection dynamic merging circuits pin

Searched for port connection dynamic merging circuits

Searched for port connection dynamic merging circuits - Viewed 1 resultViewed 1 result

Not starred A compact multiway merge sorter using VLSI linear-array... -
springerlink.com - Bookmark: edit - remove

Labels:
Notes:
www.springerlink.com/-index/p343016974207r22.pdf

Searched for port

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4477001	(port input output pin connector)	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:36
L2	6450721	connect$4 link$4 map$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:41
L3	1	"6553543".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; IBM_TDB	OR	ON	2008/01/28 19:41